



10/658 742

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re U.S. Patent No. 6,906,569 *B2*

) Serial No. 10/658,742

Inventor(s): Martin BROX

) Filed: September 10, 2003

Issue Date: June 14, 2005

) Attorney Docket No. 006410.00001

For: DIGITAL SIGNAL DELAY DEVICE

REQUEST FOR CERTIFICATE OF CORRECTION

U.S. Patent and Trademark Office
Customer Service Window
Randolph Building, Mail Stop: Certificate of Correction Branch
401 Dulany Street
Alexandria, VA 22314

**Certificate
FEB 14 2006
of Correction**

Sir:

Pursuant to 35 U.S.C. § 254 and 37 C.F.R. § 1.322, this is a request for the issuance of a Certificate of Correction in the above-identified patent. Two (2) copies of PTO Form 1050 are appended. The complete Certificate of Correction involves one page.

The mistakes identified in the appended Form occurred through no fault of the Applicant, as clearly disclosed by the records of the application, which matured into this patent. Enclosed for your convenience is the relevant portion of the Amendment filed January 5, 2005.

Issuance of the Certificate of Correction containing the corrections is respectfully requested. Since these changes are necessitated through no fault of the Applicant, no fee is believed to be associated with this request. Nonetheless, should the Patent and Trademark Office determine that a fee is required, please charge our Deposit Account No. 19-0733.

Respectfully submitted,

BANNER & WITCOFF, LTD.

Dated: February 9, 2006
By: J. M. F.
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FEB 15 2006

UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

PATENT NO.: 6,906,569 *B2*

DATED: June 14, 2005

INVENTOR(S): Martin BROX

It is certified that errors appear in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

In Column 11, Claim 11, Line 47:

Please replace "(N)," with --(IN),--

In Column 12, Claim 14, Line 11:

Please replace "claim 1" with --claim 13--

Mailing Address of Sender:

Banner & Witcoff, Ltd.
11th Floor
1001 G Street, N.W.
Washington, DC 20001-4597

U.S. PAT. NO 6,906,569

No. of add'l copies
@ \$0.50 per page

□

FORM PTO 1050 (Rev.2-93)

FEB 15 2006

UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

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DATED: June 14, 2005

INVENTOR(S): Martin BROX

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In Column 11, Claim 11, Line 47:

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In Column 12, Claim 14, Line 11:

Please replace "claim 1" with --claim 13--

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FORM PTO 1050 (Rev.2-93)

FEB 15 2006



PATENT DESIGN B&W Ref. 006410.0001

HAND CARRY Group 10

Serial/Patent No. 107658,742

Inventor: *Box*

Title: *Digital signal delay*

Date 1/5/05

Bldg

Rm

Atty/Sec: *TMFT LLC*

Client: *Digitalen technologies*

The following has been received in the U.S. Patent and Trademark Office on the date stamped hereon:

total pp Spec., including: # of Claims Request for Corrected: Filing Receipt Assignment
 # of independent claims Abstract Response to Restriction/Election Requirement
 Application Data Sheet (ADS): Initial Supplemental Sequence Listing: Diskette Paper *107658,742*
 Drawings: Formal Informal Amendment Response: OA *107658,742*
 # of distinct sheets Figs: Petition for Extension of Time until *107658,742*
 Nonpublication Request RCE w/Ext of Time: OA did
 Declaration/Power of Attorney: Executed Unexecuted Request for Approval of Drawing Changes
 Assignment w/PTO Cover Sheet Notice of Appeal & Fee
 IDS w/PTO 1449 References w/Fee Brief: Appeal & Fee Reply *JAN 05 2005*
 Preliminary Amendment Request for Oral Hearing
 Priority Claim: (Foreign or U.S. Provisional) B&W# Issue Fee Pub. Fee Adv. Pat. Copies #ordered

Notice of Allowance did

Country Appl. # Date Amendment under 37 CFR 1.312
 w/Foreign Priority Document(s) Request for Certificate of Correction
 Application: CIP Continuation Divisional Transmittal Fee Transmittal w/Auth. to Charge Deposit Acct.
 Parent SN: B&W# Certificate of Mailing
 U.S. Provisional pp Spec/Claims; Cover Sheet Check No. for \$ *107658,742*
 Response to Missing Parts/Requirements did *Replacement Sheet Fig. 1*
 Response to Notice to File Corrected Appln. Papers did *pg 3 + 4 (25 sheets)*
 Request for Expedited Foreign Filing License *25 sheets*

B&W Rev. 01/04

1 annotated sheet for Figs 1, 3 + 4 (25 sheets)

FEB 15 2006



Appl. No. 10/658,742
Amdt. Dated January 5, 2005
Reply to Non-Final Office Action of October 5, 2004

Appl. No. : 10/658,742
Applicant : Martin Brox
Filed : September 10, 2003
Title : Digital Signal Delay Device

TC/A.U. : 2816
Examiner : An T. Luu
Docket No. : 006410.00001
Confirmation No. : 3389

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

RESPONSE AND AMENDMENT

Sir:

In response to the non-final Office Action mailed October 5, 2004, please amend the instant application as follows:

Amendments to the Specification begin on page 2 of this paper.

Amendments to the Claims are reflected in the listing of claims which begins on page 3 of this paper.

Amendments to the Drawings begin on page 8 of this paper and include both an attached replacement sheet and an annotated sheet showing changes.

Remarks/Arguments begin on page 9 of this paper.

An Appendix including amended drawing figures is attached following page 13 of this paper.

Claim 9 (currently amended): The digital signal delay device according to claim 826, wherein the gates, depending on whether ~~they~~the gates are connected with a signal delay element whose output signal is inverted or non-inverted vis-à-vis the signal (IN), [-]-comprise an inverter circuit arrangement or a transfer gate circuit arrangement.

Claim 10 (currently amended): The digital signal delay device according to claim 826, wherein the gates, depending on whether ~~they~~the gates are connected with a signal delay element whose output signal is inverted or non-inverted vis-à-vis the signal (IN), [-]-comprise two inverter circuit arrangements, or a transfer gate circuit arrangement and an inverter circuit arrangement.

Claim 11 (currently amended): The digital signal delay device according to claim 826, wherein the gates, depending on whether ~~they~~the gates are connected with a signal delay element whose output signal is inverted or non-inverted vis-à-vis the signal (IN), [-]-comprise two inverter circuit arrangements and a transfer gate circuit arrangement, or three inverter circuit arrangements.

Claim 12 (original): The digital signal delay device according to ~~claim 11~~, wherein at least one of the inverter circuit arrangements is a tristate inverter circuit arrangement.

Claims 13-20 (canceled)

Claim 21 (new): The digital signal delay device according to claim 1, wherein the gates, depending on whether the gates are connected with a signal delay element whose output signal is inverted or non-inverted vis-à-vis the signal (IN), comprise an inverter circuit arrangement or a transfer gate circuit arrangement.

Claim 22 (new): The digital signal delay device according to claim 1, wherein the gates, depending on whether the gates are connected with a signal delay element whose output signal is